

MITSUBISHI

AJ71QC24-R2 Serial Communications Module

User's Manual (Hardware)

Thank you for choosing the Mitsubishi MELSEC-QnA Series of General Purpose Programmable Controllers To ensure correct use of this equipment, please read this manual carefully before operating it



IB(NA) 66610-A (9603) MEE

Related Manuals

The following manuals are available for this equipment Refer to the table given below to choose suitable manuals

Manual Name	Manual No (Type Code)
Guide Book for the AJ71QC24 Serial Communications Module	IB-66622
User's Manual for the AJ71QC24(-R2/R4) Serial Communications Module	IB-66612

Before using this equipment, please read this and related manuals thoroughly Also pay special attention to safe and correct use of the equipment

The safety precautions given in this section relate to this equipment only. For precautions regarding the programmable controller system, refer to the User's Manual for the CPU module.

The following two safety precaution categories are used in this manual

() DANGER:	Describes precautions that should be observed to pre- vent the danger of serious injury or death to the user in case of incorrect use of the equipment
	Describes precautions that should be observed to pre- vent the danger of medium or light injury to the user or physical damage to the equipment in case of incorrect use of the equipment

However, note that incorrect use denoted by " A CAUTION" may result in serious accident in some cases Make sure that all the precautions given in this section are strictly observed

Keep the manual in a safe place so that it can be referred to whenever necessary Also make sure that this manual is forwarded to the final user

[Precautions regarding system design]

∧ c	AU	TIO	N
-----	----	-----	---

• Never place the control cables and communication cables near the main circuit and power cables Never bind them with the main circuit and power cables

Make sure that they are placed at least 100 mm away from the main circuit and power cables Failure to observe this may result in malfunction of the equipment due to noise

[Precautions regarding installation]

- Make sure that the equipment is used in the operating environment specified in this manual, otherwise electric shock, fire, malfunction, damage or deterioration of the equipment may result
- When installing the equipment, make sure that the Tixing projections provided at the base of the equipment are inserted into the holes on the base unit properly If they are not properly inserted, a malfunction, breakdown or fall of the equipment may result

[Precautions regarding wiring]

▲ CAUTION

- Before connecting the cables, check the type of interface to be connected If the cables are connected to an incorrect interface, damage to the equipment or external device may result
- Take care not to allow any foreign matter, such as cutting refuse or wire bits, to enter the equipment. If they enter, fire, breakdown or malfunction may result

[Precautions regarding set-up and maintenance]

 Before cleaning the equipment, make sure the power is turned OFF If cleaning is carried out while the power is ON, breakdown or malfunction of the equipment may result

- Never attempt to disassemble or modify the equipment, otherwise breakdown, malfunction, injury or fire may result
- Before installing or removing the equipment, make sure that the power is turned OFF
- If the equipment is installed or removed while the power is ON, breakdown or malfunction of the equipment may result

[Precautions regarding disposal]

 When disposing of the equipment, treat it as industrial waste

1. General Description

This manual describes specifications and names of each part of the AJ71QC24-R2 serial communications module, which is used in conjunction with the MELSEC-QnA series programmable controller CPU

When unpacking the equipment, check that the unit listed in the table below is present

Product Name	Qty.
AJ71QC24-R2 serial communications module	1

3. Specifications

31 Communications

The communications specifications of the equipment is given below

Γ			Specifi	cations	
ltem -			CH1	CH2	
	In	terface.	RS-2	232C	
	Communi	cations method.	Full-duplex/Half-c	luplex (selectable)	
	Syncl	nronization.	Start-sto	p system	
	Ba	aud rate:	300 to 19200 BPS (0	CH1 and CH2 in total)	
Dat	ta format	Start bit.		1	
		Data bit.	7,	/8	
		Parity bit.	1 (yes)	/ 0 (no)	
		Stop bit.	1,	/2	
Error Parity check.		Parity check.		even) / No	
det	detection. Sum check code.		Yes / No		
Co	ntrol	DTR/DSR.	Yes / No		
me	thod.	DC code.	Yes (DC1/DC3, DC2/DC4) / No		
	Writing	to EEPROM.	100,000 times for the same area (Max.)		
Ë	Indepen	Dedicated protocol			
ecti,	dent mode	Modeless protocol	11	11	
Vetwork connection:	deni mode	Bidirectional protocol			
ιž	Interlock	Dedicated protocol	Į		
two	mode	Modeless protocol	Interlock oper	ation disabled	
Ž	Bidirectional protocol				
Alle	owable com	munications distance.	15 m or less		
	Power	consumption.	5 VDC, 0.2A		
	Numbe	r of I/O points.	32 points		
	. 1	Veight.	0.3	7 kg	

3 2 RS-232C Interface

3 2.1 Connector Pin Assignment

		Pin No	Signal Name	Signal Code	Signal Direction (AJ71QC24-R2 ← External Device)
2•	014 015	1	Frame ground	FG	•>
3•	015	2	Sent data	SD (TXD)	
4 •	017	3	Received data	RD (RXD)	<
5 •	018	4	Request to send	RS (RTS)	
6 ● 7 ●	019	5	Clear to send	CS (CTS)	
8 •	•20	6	Data set ready	DSR (DR)	•
90	021	7	Signal ground	SG	← →
100	022	8	Carrier detection	CD	
110	023 024	20	Data terminal ready	DTR (ER)	
120 130	024				

The following type of the RS-232C connector is used. The counter connector must match this connector

25-pin D-sub (female) screw type 17LE-13250-22-D2AC (DDK ELECTRONICS LTD)

3 2 2 RS-232C Interface Cable

The RS-232C interface cable must be of 15 m or shorter and conform to the RS-232C standard

(Recommended cable) 7/0 1271 [P HRV-SV

> Number of twisted wires In the case of 13 twisted wires (7/0 127 13P HRV-SV)

2. System Configuration

2.1 Applicable System (Applicable CPU module and allowable number of modules)

The following table shows programmable controller CPU module and network module (for remote station) which can be used with the equipment. The allowable number of modules to be installed is also given

Applicable Module		Allowable Number	Remarks	
CPU module	Q2A (S1) Q3A Q4A	Not limited	The allowable number of mod- ules is determined according to the number of available in-	
Network module	AJ72QLP25 AJ72QBR15		put/output signals of the CPU module and remote station	

4. Name of Each Part



No.	Name			Description																
()	LED		RUN	Operation state																
				ON Normal																
	RUN O CPUR/W O	OCH1 ERR OCH2 ERR OO OONEU OONAK OOPROCH2 OOSIO OOSD WAIT OORD				OCH1 ERR OCH2 ERR	O CH1 ERR O CH2 ERR	O CH1 ERR O CH2 ERR	O CH1 ERR O CH2 ERR	OCH1 EBB	O CH1 ERR O CH2 EBB	OCH1 ERR		OFF Abnormal						
	, i i i i i i i i i i i i i i i i i i i									CPUR/W	Communications state with PC									
							ON Communications is in													
						O ACK O NAK	O ACK O NAK		progress											
ļ								OC/N OP/S CH2		OFF. Communications is in halt.										
	CH1 PROO SIOQ		NEU	Neutral state (CH1/2)																
	SD WAIT O		O SD WAIT O SD O RD		ON Transmission sequence is ini-															
	SD WAIT O			OBD _		tialized														
				OFF Reception of ENQ is com-																
				plete																
			ACK	ACK transmission state (CH1/2)																
				ON When ACK is transmitted																
				OFF. When NAK is transmitted																
			NAK	NAK transmission state (CH1/2)																
				ON NAK is transmitted																
				OFF. ACK is transmitted																
			Ć/N	Communications state between CH1/																
				0/11	2 and programmable controller CPU															
						ON Abnormal														
				OFF, Normal																
				P/S			P/S	Parity/sum check error (CH1/2)												
					ON Error															
				OFF Normal																
																		-	PRO	Protocol error (CH1/2)
			1110	ON Error																
ļ	ļ		ļ	OFF. Normal																
			SIO	SIO error (CH1/2)																
				ON Received data is disposed of																
				due to overrun, framing error																
				or OS receive area full																
				OFF Normal																
			SD WAIT	Wait state																
				ON Awaiting data transmission																
				OFF Transmission is started.																
			SD	Transmission state (CH1/2)																
				Blink Data transmission is in																
				progress																
			- BD	Reception state (CH1/2)																
1				Blink. Data reception is in progress																
1			CH1/2 ERR	Error (CH1/2)																
1				ON Switch setting error, mode																
				switch error, transmission er-																
				ror, reception error, on-de-																
				mand error																
L	I		<u> </u>	OFF Normal																

	Station No	Lleod	to or	t the station	No		
2	station No	<settin< th=""><th></th><th></th><th>UN</th><th></th><th></th></settin<>			UN		
	setting switch	 0 to	~	ange>			
	X10 X1			ed to set the	tone d	iait of th	e etation
	「洗」、洗」 STATION	XIU	No			ight of th	ie station
	NO	x1		ed to set the	unite d	liait of th	e station
		~ 1	No.		unita u	igit of t	io station
3	Mode setting switch	Used		t mode.			• ••• •••
	-	Mode			Descri	ption	
	СН	0	Set	ting disabled		·	
	67897	1				[I	Format 1
	THE MODE	2			ASCII r	nada []	Format 2
	61272	3	Ded	icated protocol	ASOIN		Format 3
· ·		4	1			i	ormat 4
		5	1		Binary	mode I	Format 5
		6	Mo	deless proto	col		
1		7	Bid	rectional pro	tocol		
		8					
		to	Set	ting disabled	l .		
		D					
		<u> </u>		M/RAM/swit	ch test		
		F		ap test			
(4)	Communications			the following o	communi		
	parameter setting	Switch					tate
	switch	CHIC	CH2			OFF	ON
		SWO	1	Operation r	node	Indepen-	
			0	Data kit		dent	disabled
	88	SWO		Data bit		7 bits	8 bits
		SWO		Parity bit	(odd)	No	Yes Even
		SWO	-	Parity (even Stop bit	/000}	Odd 1 bit	2 bits
		SWO		Sum check		No	Yes
		<u> </u>		Writing duri			-
		SWC	7	operation	ng	Disablec	Enabled
		SWC	-	Setting cha	nge	Disablec	Enabled
		SWC	9				
l	l	l to		Baud rate		Refer to	o *1
		SW1					
		SW1	3				all the
		to	_			1	ches to
		SW1	5				DFF)

*1 Baud rate

Baud rate (BPS)	300	600	1200	2400	4800	9600	19200
SW09	OFF	ON	OFF	ON	OFF	ON	OFF
SW10	OFF	OFF	ON	ON	OFF	OFF	ON
SW11	OFF	OFF	OFF	OFF	ON	ON	ON
SW12	OFF	OFF	OFF	OFF	OFF	OFF	OFF

No.	Name	Description
6	RS-232C interface	Used to connect the equipment to external
		device.

5. Handling Precautions

The equipment must be secured using screws Allowable tightening torque range for the screws is given below

Screw	Tightening Torque
Unit fixing screws (M4)	78 to 117 N cm (8 to 12 kg cm)

6.2 Self-Test

The following functions are provided to check whether the equipment can operate correctly, without being connected to an external device

Make sure that the test is carried out with the programmable controller CPU in stop state. Also make sure that the power is turned OFF before connecting the cables and setting the switches

6 2 1 ROM/RAM/Switch Test

Setting the mode setting switch . Set the mode setting switch for CH2 to "E" The mode setting switch for CH1 must be set to the No (1 to 7) corresponding to the mode in which data is to be transferred between the equipment and external device after completion of the test

> · Set the communications parameter switches according to the communications specifications for the external device

Starting the ROM/RAM switch test

• Turn ON the power to the programmable controller CPU or reset the CPU to start the test

Checking the LED indicators

Check Item		LED Name	Normal	Abnormal
(Test end)		SD WAIT *1	ÓN	
ROM che	ck	CH1 ERR	OFF	ON
RAM che	ck	CH2 ERR	OFF	ON
	Station No	Located just below CH2 ERR	OFF	ON
Switch	Mode	C/N *2	OFF	ON
check	Communications parameter	P/S *2	OFF	ON
Interlock	Mode	CH1-PRO	OFF	ON
setting check	Communications parameter	CH1-SIO	OFF	ON

*1 LED for both CH1 and CH2

*2 LED for the interface where the setting error is occurring

6.2 2 Wrap Test

Ò

Connecting the cables

Connect the RS-232C interface as shown below

	-		
	AJ71QC-	24-R2 Side	Coble Connection
	Signal Name	Pin No.	Cable Connection
	FG	1	
	SD	2]
	RD	3]
	RS	4]]
	CS	5]
	DSR	6]•]
	SG	7	
	CD	8]₄
ļ	DTR	20	
ł			
Setting	the mode settin	g switch	
Ļ	Set the mode	e setting switc	h for both CH1 and CH2 to "F"
Starting	wrap test		
		power to the U to start the f	programmable controller CPU or

reset the CPU to start the test

6 1 Set-up Procedure

This section describes how to start up the equipment

For a detail description, refer to the User's Manual for the AJ71QC24(-R2/R4) Serial Communications Module

6. Starting up the Equipment



Ŷ			
Ch	ecking the LED indica	tors	
	Check Item	Norm	al
	Communications	CH1-C/N	OF
		CDUEDAN	ON

Of a management of the second				
with programmable	CPU R/W	ON (dark)	CH1-C/N	ON
controller CPU	CH1 NEU, ACK, NAK	Blinks in turn.		
	CH1.ERR	OFF		
RS-232-C (CH1)	CH1-SD	Blink	CH1 ERR	ON
communications	CH1-RD			
	CH2.ERR	OFF		
RS-232-C (CH2)	CH2-SD	Blink	CH2 ERR	ON
communications	CH2-RD	DILIK		

Abnormal

Ending the wrap test

• Turn OFF the power

After the test is complete, change the mode setting switch to enable data transfer with the external device

(Make sure that the mode setting switch for the interface via which data transfer is not to be carried out is set to one of 1 to 7)

101 7. External Wiring

7 1 Connecting the RS-232C Interface

Typical connecting method for the RS-232C interface is described below

(1) An example of connecting to an external device which is capable of turning ON/OFF the CD signal (pin 8)

AJ71QC2	4-R2 Side	Connection and Signal	External Device
Signal Name	Pin No	Direction (Example)	Signal Name
FG	1	*	FG
SD (TXD)	2		SD (TXD)
RD (RXD)	3	+	RD (RXD)
RS	4		RS
CS (CTS)	5		CS (CTS)
DSR (DR)	6	\sim \sim \sim	DSR (DR)
SG	7		SG
CD	8		CD
DTR (ER)	20	\succ	DTR (ER)

(2) An example of connecting to an external device which is not capable of turning ON/OFF the CD signal (pin 8)

(a) An example for DC code control or DTR/DSR control

AJ71QC2	4-R2 Side	Connection and Signal	External Device
Signal Name	Pin No	Direction (Example)	Signal Name
FG	1	← →	FG
SD (TXD)	2		SD (TXD)
RD (RXD)	3	+	RD (RXD)
RS	4		RS
CS (CTS)	5	⊣ ₄/ └_▶	CS (CTS)
DSR (DR)	6		DSR (DR)
SG	7		SG
CD	8		CD
DTR (ER)	20	$\gamma \gamma$	DTR (ER)

(b) An example for DC code control

AJ71QC2	4-R2 Side	Connection and Signal	External Device
Signal Name	Pin No	Direction (Example)	Signal Name
FG	1	*	FG
SD (TXD)	2		SD (TXD)
RD (RXD)	3	+	RD (RXD)
RS	4		RS
CS (CTS)	5	┥ ┥╴ └→	CS (CTS)
DSR (DR)	6	┉╴┨╺╋───┐ ┌──►	DSR (DR)
SG	7	→	SG
CD	8	\neg $ $ $ $	CD
DTR (ER)	20		DTR (ER)

8. Outside Dimension



